## ABSTRACT OF THE DISCLOSURE

The present invention relates to a sensor apparatus capable of outputting an accurate sensor signal irrespective of a variation of power supply voltage. In the sensor apparatus, when an A/D conversion circuit produces and outputs digital data, a switch circuit is placed into a first switching condition so that a constant voltage is applied from a constant-voltage circuit to an oscillation circuit, and a stabilized oscillation frequency is outputted from the oscillation circuit to the A/D conversion circuit. Thus, even if a power supply voltage varies, the A/D conversion circuit carries out sampling processing on analog data on the basis of the stabilized oscillation frequency from the oscillation circuit to produce and output the digital data.

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